

Time (processor cycles) →

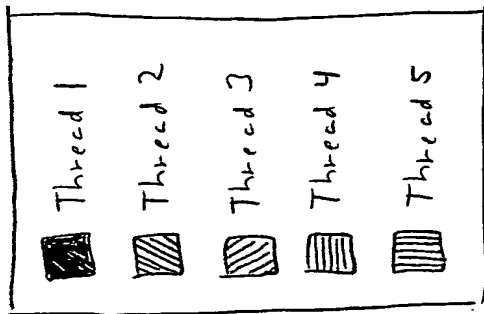
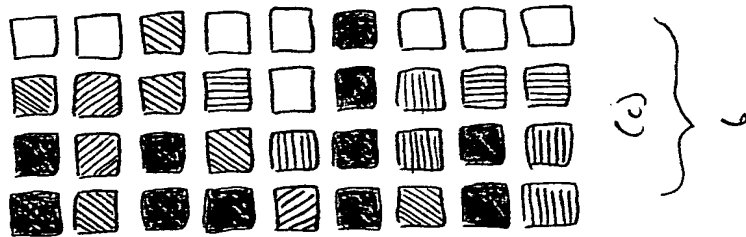
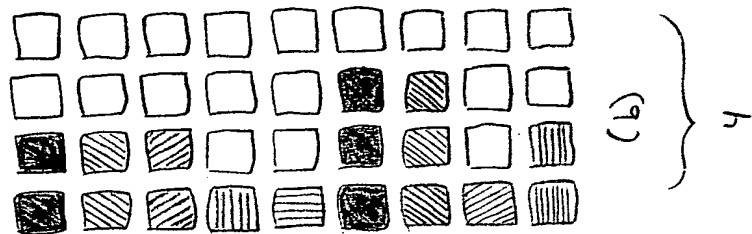
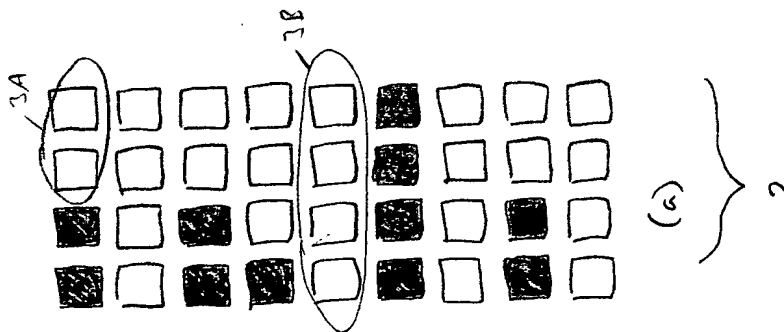


Fig. 1

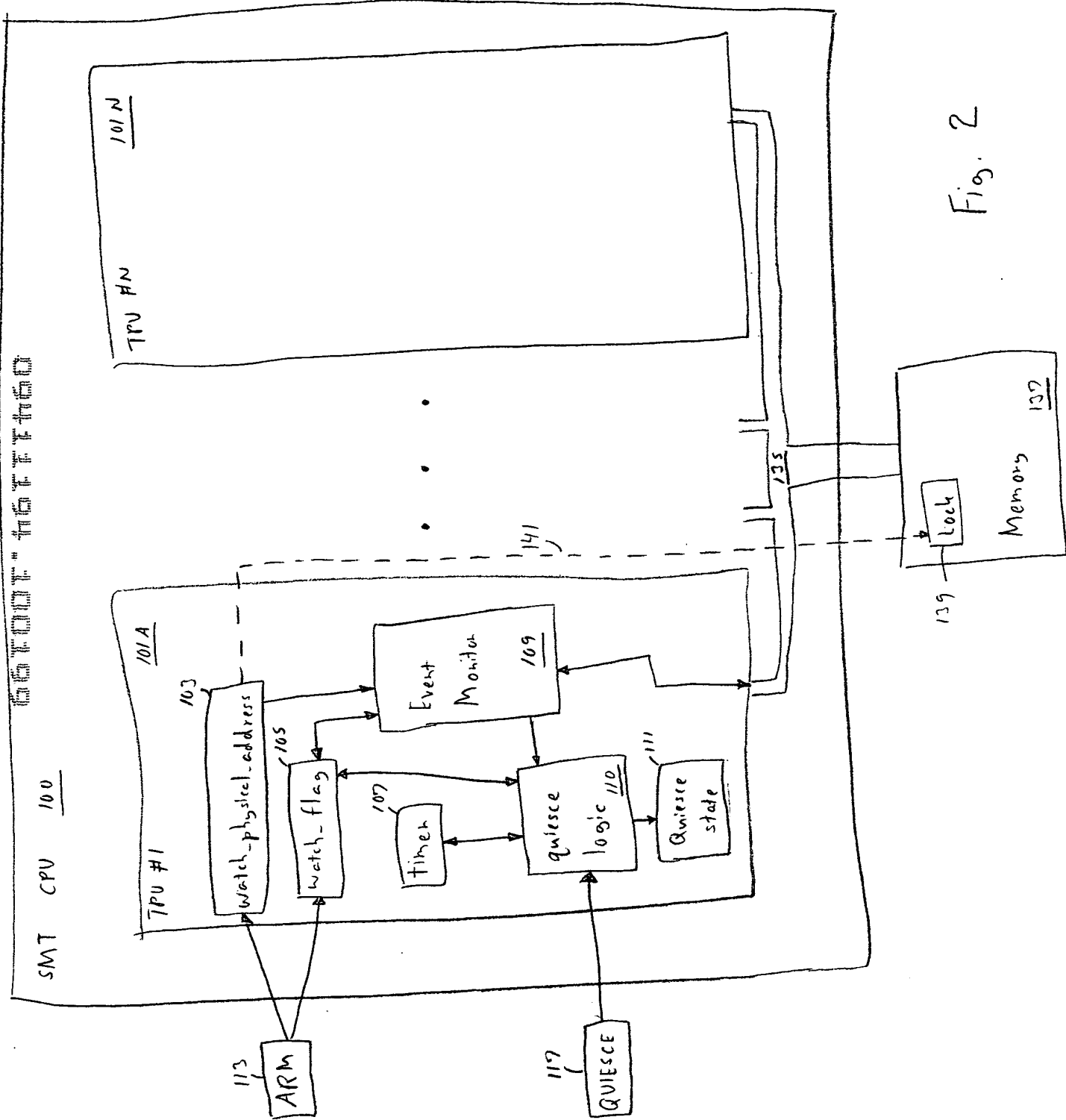


Fig. 2

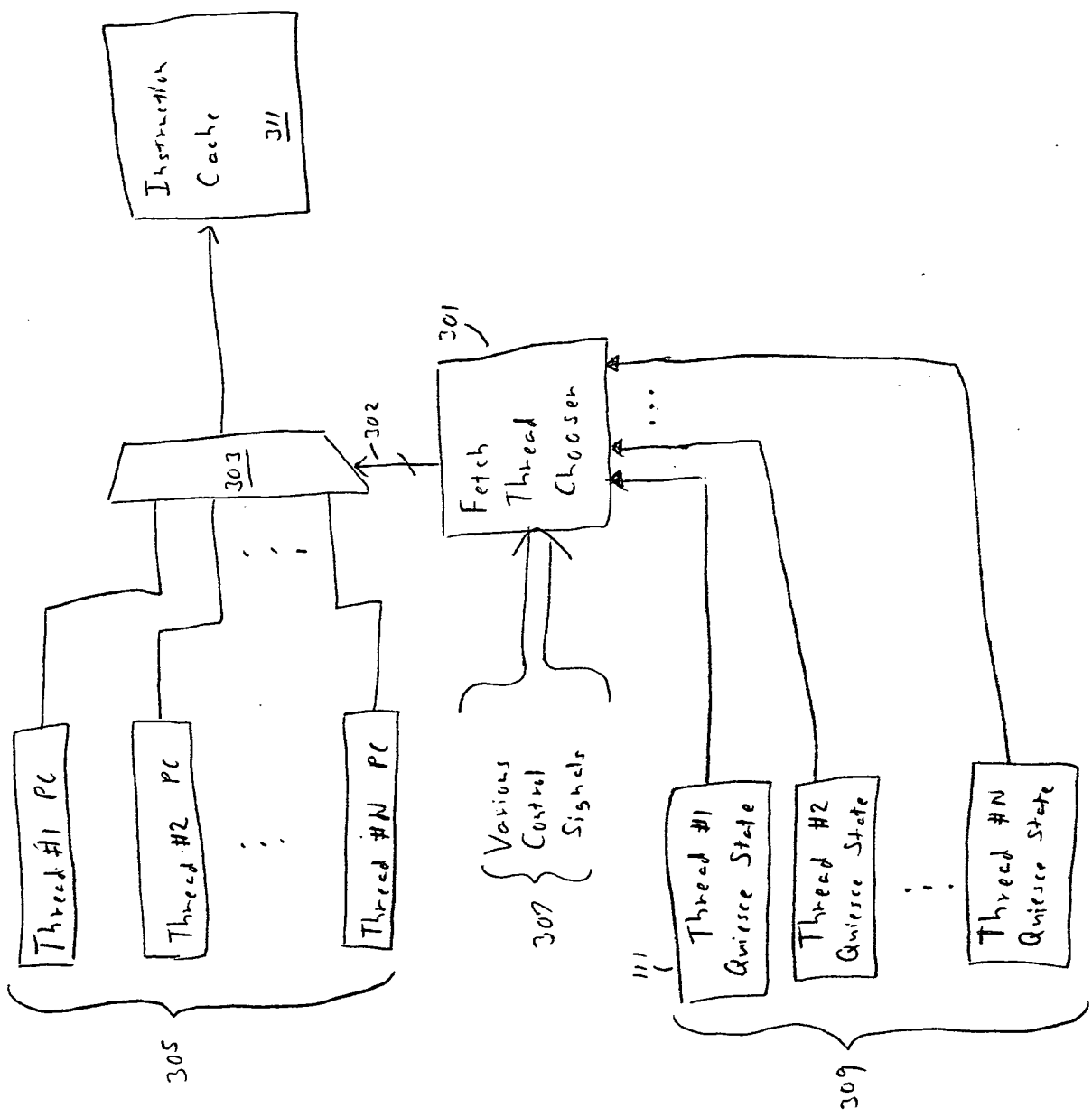


Fig. 3

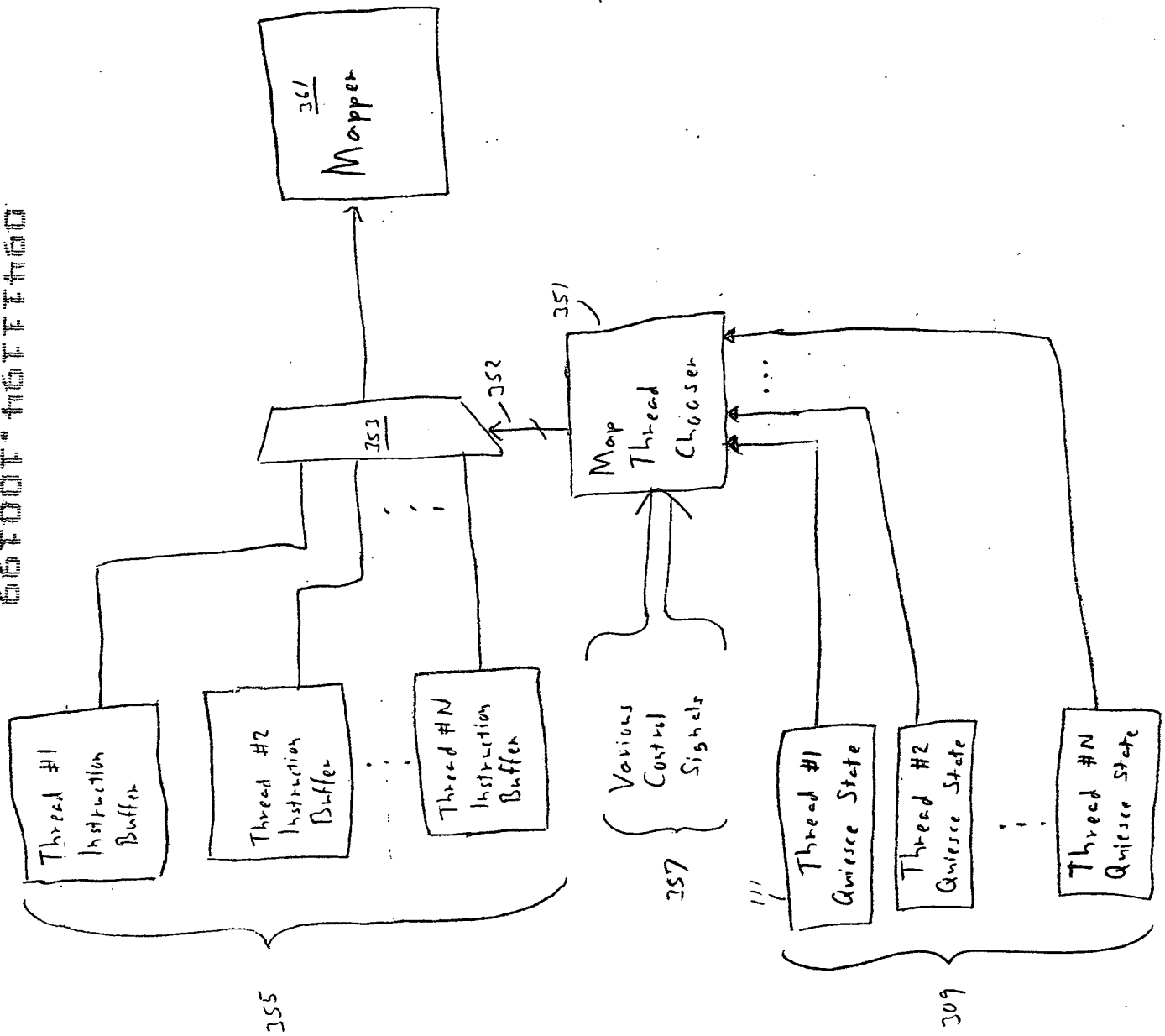


Fig. 4

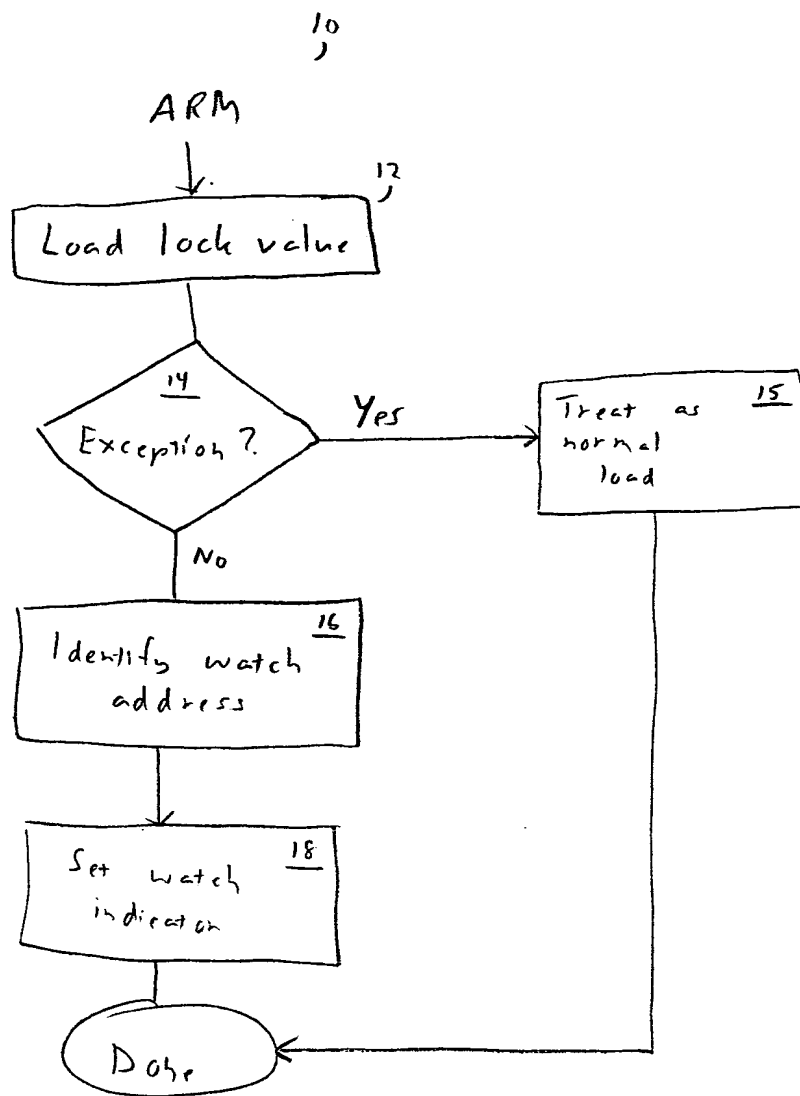


Fig. 5

Computer 400

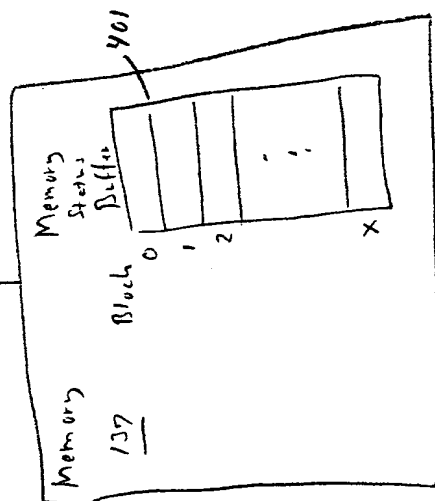
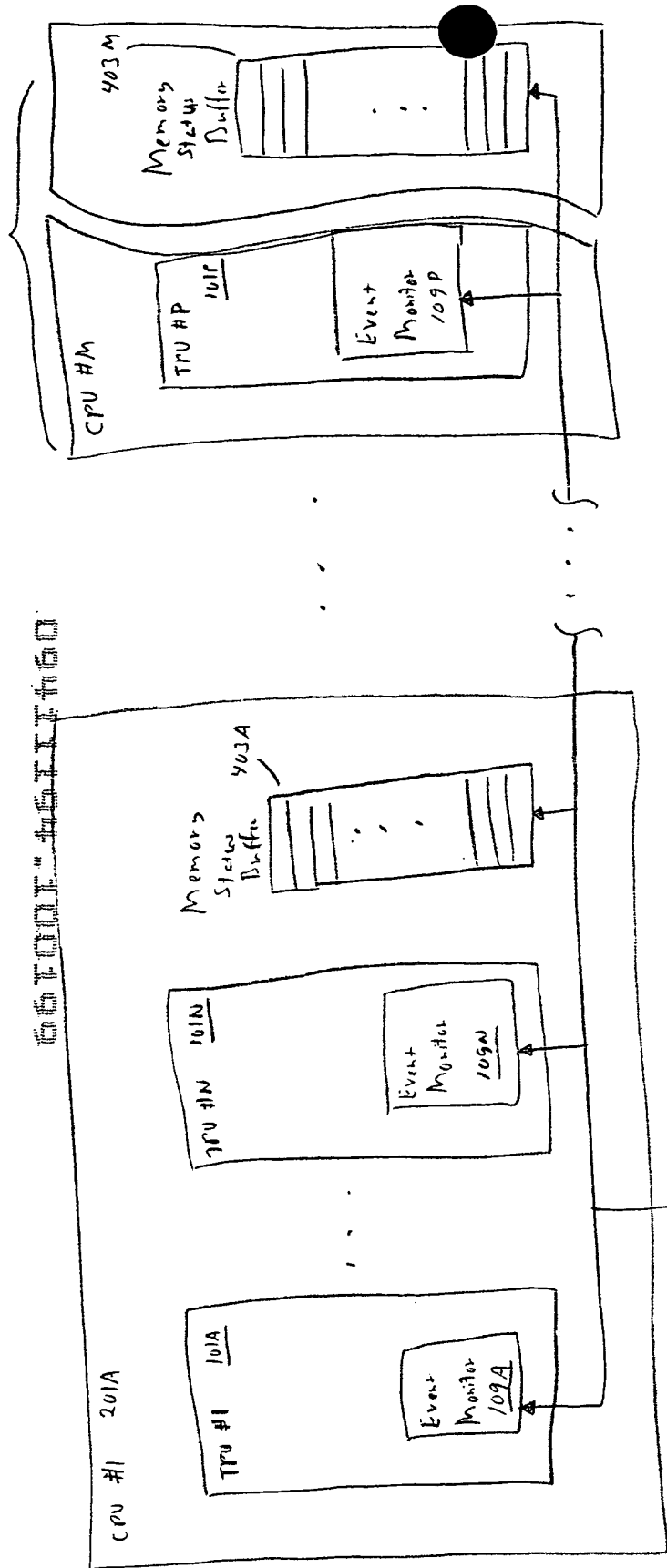


Fig. 6

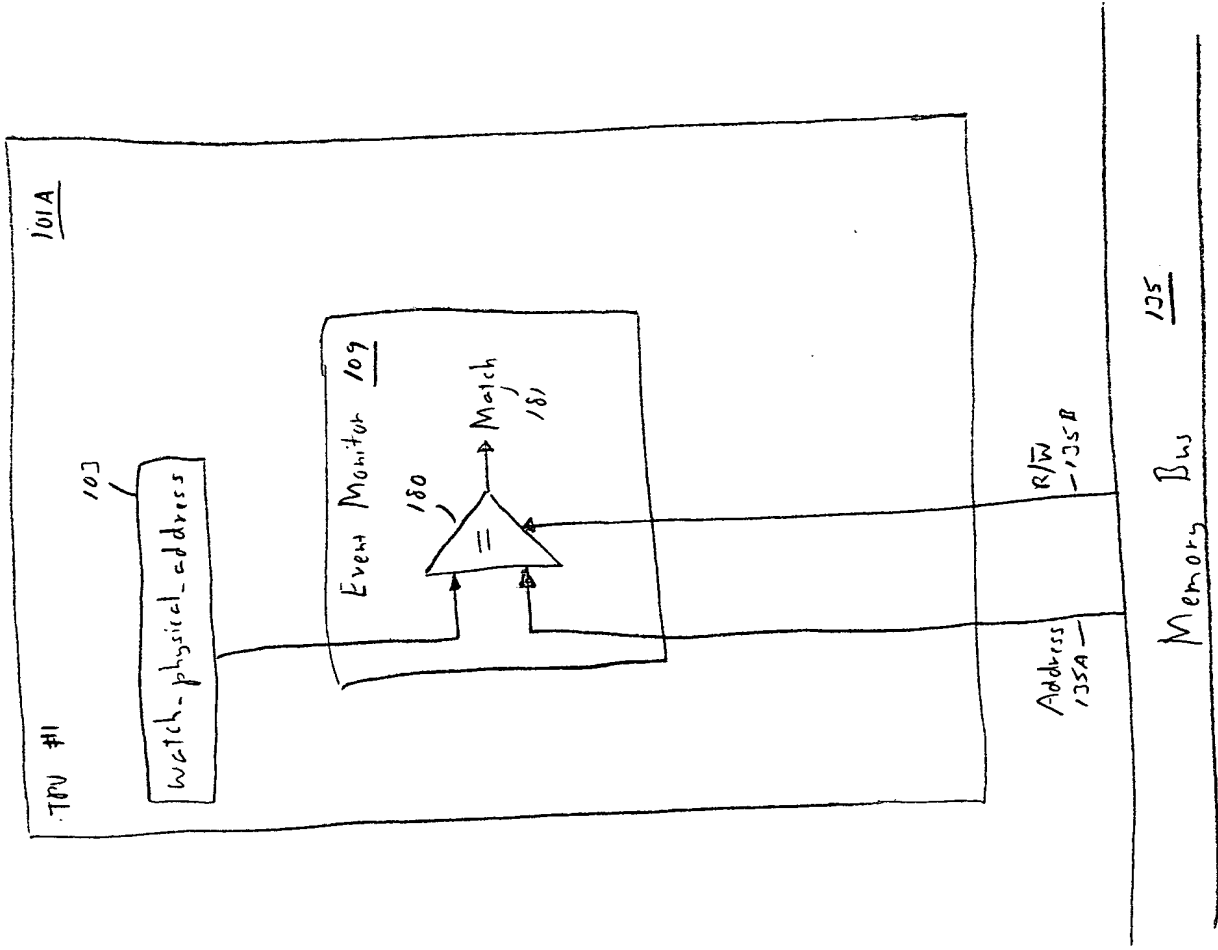


Fig 7

20
QUIESCE

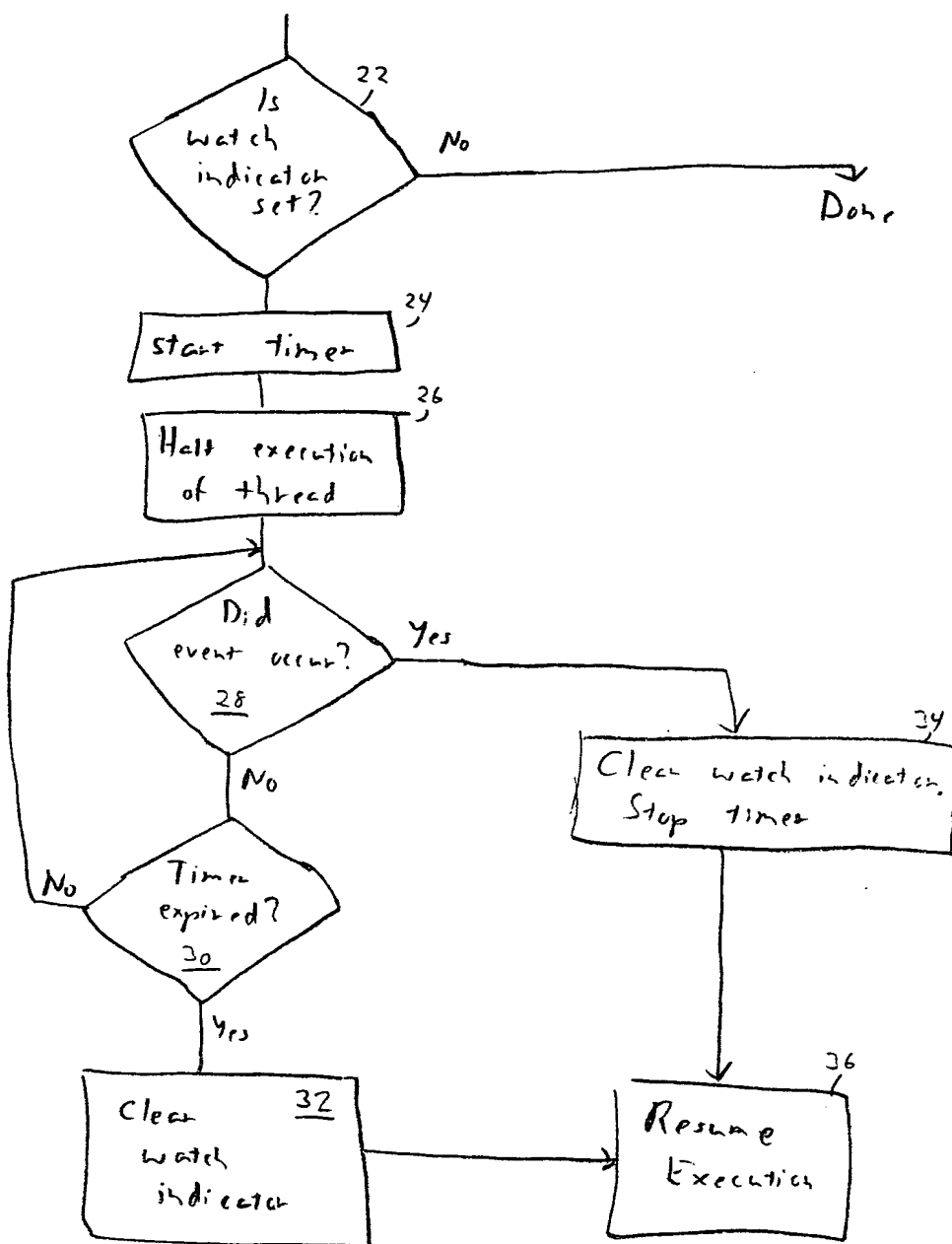


Fig. 8

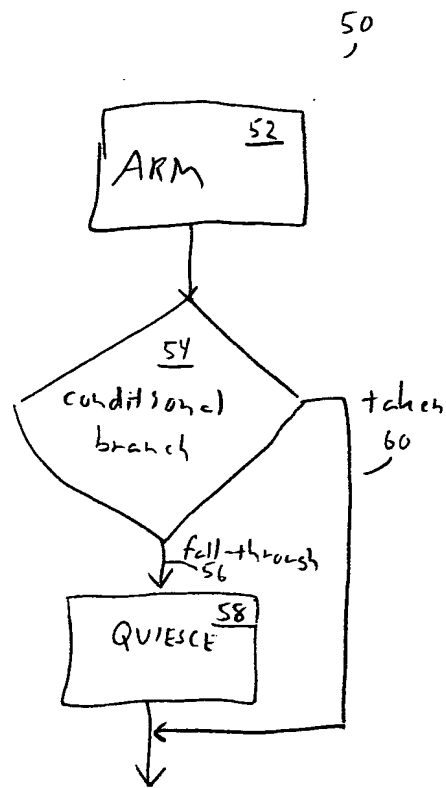


Fig. 9